

## One Day Workshop On “Verilog Hardware Description Language (VHDL) Coding”

One Day Workshop on “**Verilog Hardware Description Language (VHDL) Coding**” was conducted on **Saturday 28/12/2019**; total 50 students from B.Sc. (Reg) of MIT ACSC attended this workshop. The invited guest speaker of this workshop was **Prof. Umesh N. Kothavade and Prof. Nirasha J. Ithape from Modern college Shivajinagar Pune**. The session started at 11:00 AM with felicitation of the guest **Prof. Umesh N. Kothavade** in Presence of HOD of Electronics Dept. **Prof. Sunil Mahajan** and HOD’s of various Departments, Faculties of electronics and different courses were also present.

The felicitation was followed by introduction of the guest by T.Y. B.Sc. student Rajat Ghorpade and Sayalli Thigale, there after Prof. Umesh Kothavade presented his talk on VHDL with the help of power point presentation, in which the roll of Analog Electronics, Digital Electronics, applications of VHDL was explained in detail. He discussed about the skills to be developed by students to become a competent candidate to grab the job opportunities in industry and after theory session, practical session was conducted. The speech was concluded with the Question and answer session in which doubts of students got satisfactory cleared. HOD of Electronics Dept Prof. Sunil Mahajan also interacted with the students. Prajkta Raskar student of T.Y.B.Sc. Electronics Proposed the Vote of thanks at the end of session.

